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Docket No. 56426

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: M. Brett et al.

EXPRESS MAIL LABEL: EL 835032915US

FILED: HEREWITH

FOR: METHOD AND CIRCUIT FOR PICTURE-IN-PICTURE
SUPERIMPOSITION

THE HONORABLE COMMISSIONER OF PATENTS AND TRADEMARKS
WASHINGTON, DC 20231

SIR:

PRELIMINARY AMENDMENT

Applicants file herewith the above-identified application. Please amend the application as follows.

IN THE CLAIMS

Please cancel claims 1-10 without prejudice.

Please add the following new claims.

11. A method for picture-in-picture insertion, in which a sequence of decimated inset pictures is written to a memory device and is read out for insertion into a sequence of main pictures,

wherein inset pictures are written to the memory device in a circulating manner as fields under continuously incremented write addresses, the inset pictures being written to corresponding memory segments beginning at corresponding writing start addresses, in that the writing start address of each written-in field is stored,

in that, each time the write address is incremented, by comparison of the respective instantaneous write address with a previously stored writing start address, an overtake signal is formed which indicates whether the respective writing start address is reached again and the